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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Kadono et al.

Serial No.: 09/535,233

Filed: March 24, 2000

For: A Method Of Manufacturing A  
Semiconductor Device

Art Unit: 2823

Examiner: W. Coleman

I hereby certify that this correspondence is being  
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Commissioner for Patents  
Washington, D.C. 20231

on May 28, 2002

Rachelle Hammerquist

Signature

Date

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby call the Examiner's attention to documents listed to on the attached form, which documents may be material to the examination of this application. Copies of the references are enclosed herewith for the Examiner's consideration.

No inference should be drawn that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field

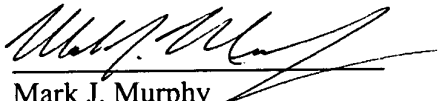
of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

Please charge our Deposit Account No. 50/1039 for any necessary IDS filing fee to the extent any enclosed funds are insufficient.

Respectfully submitted,

Date: May 28, 2002

  
Mark J. Murphy  
Registration No.:34,225

COOK, ALEX, McFARRON, MANZO,  
CUMMINGS & MEHLER, LTD.  
200 West Adams Street  
Suite 2850  
Chicago, Illinois 60606  
(312) 236-8500